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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/608,580	06/30/2000	Frankie F Roohparvar	400.006US01	9345

7590 01/24/2003

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EXAMINER

PHAN, TRONG Q

ART UNIT	PAPER NUMBER
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2818

DATE MAILED: 01/24/2003

Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary

Application No.

09/608,580

Applicant(s)

ROOHPARVAR

Examiner

TRONG PHAN

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-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE three MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136 (a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133).
- Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☒ Responsive to communication(s) filed on Dec 9, 2002.
- 2a) ☒ This action is **FINAL**. 2b) ☐ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11; 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 1-27 is/are pending in the application.
- 4a) Of the above, claim(s) _____ is/are withdrawn from consideration.
- 5) ☐ Claim(s) _____ is/are allowed.
- 6) ☒ Claim(s) 1-27 is/are rejected.
- 7) ☐ Claim(s) _____ is/are objected to.
- 8) ☐ Claims _____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☐ The drawing(s) filed on _____ is/are a) ☐ accepted or b) ☐ objected to by the Examiner.
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
- 11) ☐ The proposed drawing correction filed on _____ is: a) ☐ approved b) ☐ disapproved by the Examiner.
If approved, corrected drawings are required in reply to this Office action.
- 12) ☐ The oath or declaration is objected to by the Examiner.

Priority under 35 U.S.C. §§ 119 and 120

- 13) ☐ Acknowledgement is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
a) ☐ All b) ☐ Some* c) ☐ None of:
- ☐ Certified copies of the priority documents have been received.
 - ☐ Certified copies of the priority documents have been received in Application No. _____.
 - ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

*See the attached detailed Office action for a list of the certified copies not received.

- 14) ☐ Acknowledgement is made of a claim for domestic priority under 35 U.S.C. § 119(e).
a) ☐ The translation of the foreign language provisional application has been received.
- 15) ☐ Acknowledgement is made of a claim for domestic priority under 35 U.S.C. §§ 120 and/or 121.

Attachment(s)

- ☐ Notice of References Cited (PTO-892)
- ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
- ☐ Information Disclosure Statement(s) (PTO-1449) Paper No(s). _____
- ☐ Interview Summary (PTO-413) Paper No(s). _____
- ☐ Notice of Informal Patent Application (PTO-152)
- ☐ Other:

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Claim Rejections - 35 USC § 112

1. The following is a quotation of the first paragraph of 35 U.S.C. 112:

The specification shall contain a written description of the invention, and of the manner and process of making and using it, in such full, clear, concise, and exact terms as to enable any person skilled in the art to which it pertains, or with which it is most nearly connected, to make and use the same and shall set forth the best mode contemplated by the inventor of carrying out his invention.

2. Claims 2, 19-20 and 22-23 are rejected under 35 U.S.C. 112, first paragraph, as containing subject matter which was not described in the specification in such a way as to enable one skilled in the art to which it pertains, or with which it is most nearly connected, to make and/or use the invention.

The features as recited in claims 2, 19-20 and 22-23 are not understood because they are not described in the specification.

Claim Rejections - 35 USC § 102

3. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

4. Claims 1-13 are, insofar as understood, rejected under 35 U.S.C. 102(b) as being anticipated by Cowles et al., 5,263,003.

Cowles et al., 5,263,003, discloses in Figs. 1-3 a non-volatile flash memory system (see lines 9-21, column 1) comprising:

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system controller 61, as shown in Fig. 2, including the serial I/O (SIO) communication controller 44 which services two synchronous serial communication channels (see lines 24-27, column 5) and clock circuit 60 that contains the main system clock and a real time clock (see lines 56-58, column 6); therefore, it must be inherently synchronous non-volatile flash memory system;

flash memory 55 comprising first and second flash memory banks 71 and 72 (see lines 1-2, column 8);

bank address generator 86; wherein:

the write (programming) operation on a write cycle and read operation on a read cycle consecutively following the write cycle (see lines 40-62, column 7);

only one of first and second memory banks 71 and 72 being alternately read at a time (see lines 49-60, column 9);

write/read commands (see lines 10-12, column 13 and lines 8-9, column 15);

duplicate copies of data in flash memory 55 can be updated/latched/hold (see lines 13-21, column 12).

5. Claims 14-27 are, insofar as understood, rejected under 35 U.S.C. 102(b) as being anticipated by Patel, 5,539,696.

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Patel, 5,539,696, discloses in Fig. 1 a synchronous memory system 10 which can be used not only with SDRAM cell array 14 but also with nonvolatile EEPROM devices (see lines 22-25, column 4) comprising: digital processor 12 in communication with synchronous non-volatile EEPROM array 14 via bi-directional data bus 18 which is coupled to input/output data buffer circuit I/O 26; write latches 110, as shown in Fig. 3, being coupled to input/output data buffer circuit I/O 26 in Fig. 1 through input buffer 100, comprising latches 108, 106, 104 and 102, and data bus D0 ... Dm-1.

Response to Arguments

6. Applicant's arguments filed 12/09/02 have been fully considered but they are not persuasive.

A) The rejection of claims 2, 19-20 and 22-23 under 35 USC 112, first paragraph, as set forth above, is totally still proper because all the features as recited in claims 2, 19-20 and 22-23 are not described in the specification and Applicant has not pointed out to which part of the specification that all the features as recited in claims 2, 19-20 and 22-23 are described.

B) Cowles et al., 5,263,003, does disclose in Figs. 1-3 a non-volatile flash memory system (see lines 9-21, column 1) comprising: system controller 61, as shown in Fig. 2, including the serial I/O (SIO) communication controller 44 which

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services two synchronous serial communication channels (see lines 24-27, column 5) and clock circuit 60 that contains the main system clock and a real time clock (see lines 56-58, column 6); therefore, Figs. 1-3 of Cowles et al., 5,263,003, must be inherently a synchronous non-volatile flash memory.

Accordingly, the rejection of claims 1-13 under 35 USC 102(b) as being anticipated by Cowles et al., 5,263,003, as set forth above is totally proper.

C) Patel, 5,539,696, does disclose in Fig. 1 a synchronous memory system 10 having write latches 110, as shown in Fig. 3, being coupled to input/output data buffer circuit I/O 26 as shown in Fig. 1 through input buffer 100, comprising latches 108, 106, 104 and 102, and data bus D0 ... Dm-1.

Accordingly, the rejection of claims 14-27 under 35 USC 102(b) as being anticipated by Patel, 5,539,696, as set forth above is totally proper.

Conclusion

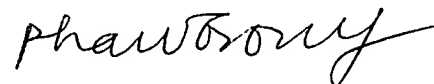
7. Applicant's amendment necessitated the new ground(s) of rejection presented in this Office action. Accordingly, **THIS ACTION IS MADE FINAL**. See MPEP § 706.07(a). Applicant is reminded of the extension of time policy as set forth in 37 CFR 1.136(a).

A shortened statutory period for reply to this final action is set to expire **THREE MONTHS** from the mailing date of this action. In the event a first reply is filed within **TWO MONTHS** of the mailing date of this final action and the advisory

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action is not mailed until after the end of the THREE-MONTH shortened statutory period, then the shortened statutory period will expire on the date the advisory action is mailed, and any extension fee pursuant to 37 CFR 1.136(a) will be calculated from the mailing date of the advisory action. In no event, however, will the statutory period for reply expire later than SIX MONTHS from the date of this final action.

8. Any inquiry concerning this communication or earlier communications from the examiner should be directed to TRONG PHAN whose telephone number is (703) 308-4870 and email address is trong.phan@uspto.gov

A handwritten signature in cursive script, appearing to read 'Phan Trong', is written in black ink.

**TRONG PHAN
PRIMARY EXAMINER**

January 22, 2003